

Notice of References Cited

Application/Control No.

09/927,566

Applicant(s)/Patent Under
Reexamination
CHOE, SWEE YEW

Examiner

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Art Unit

2819

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-4247791	01-1981	Rovell	307/355
	B	US-5859548	01-1999	Kong	326/113
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Won et al., "Modified Half Rail Differential Logic for Reduced Internal Logic Swing", 3/1998, IEEE 0-7803-4455-3/98, PP. II-157 - II-160.
	V	Jung et al., "Modular charge recycling pass transistor logic", 3/2000, Electronics Letters, Vol. 36, No. 5, pp. 404-405.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.